## 10/542727 IC12 Rec'd PCT/PTC 2 0 IIII 2005

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								July 20.	July 20, 2005		
STATEMENT BY APPLICANT						First Named Inventor		Takayuki NOTO et al.			
STATEMENT OF APPLICANT					Art Unit						
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STAT	EMENT	BY API	PLICANT	First Named Inventor	Takayuki NOTO		
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Va		Takayuki KAWAHARA et al., "Low-Power Chip Interconnection by Dynamic Termination", IEEE J. of Solid-State Circuits, Vol. 30, No. 9, 30 September 1995 (30.09.95), pages 1030 to 1034, Figs. 2, 3.	
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